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CD4093B Types

CMOS Quad 2-Input NAND Schmitt Triggers

High Voltage Types (20 Volt Rating)

Each device consists of four Schmitt trigger inverters. Each inverter functions as a two-input NAND gate with Schmitt trigger action on both inputs. The gate exhibits an efficient pulse to square-wave and regenerative action. The difference between the average voltage (Vavg) and the output voltage (Vout) is defined as hysteresis voltage (Vh) (see Fig. 2).

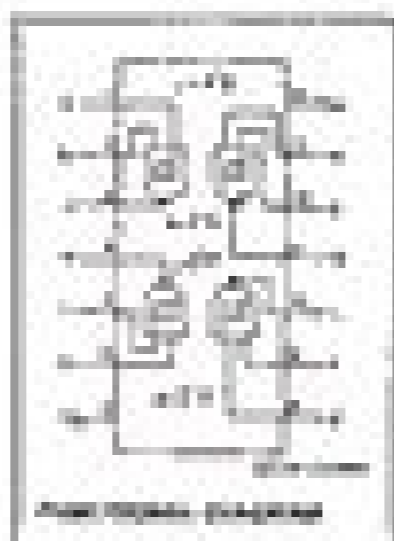
The CD4093B types are supplied in 16-pin DIP and 16-pin SOIC packages. The 16-pin DIP package is suitable for surface mounting. The 16-pin SOIC package is suitable for surface mounting.

Features

- Schmitt trigger action on both inputs with no external components
- Propagation delay: typically 50 ns at VDD = 15 V and 100 ns at VDD = 5 V
- Input resistance greater than 100 MΩ
- No load up input current 500 nA
- Standardized, symmetrical output characteristics
- 100% tested for gate-to-gate output at 20 V
- Maximum input current of 1 nA at 15 V over full package temperature range, 100 nA at 15 V and 25°C
- 5 V, 10 V, and 15 V gate-to-gate testing
- Meets all requirements of JEDEC Standard No. 10B, "Standard Specification for Description of 9 Series CMOS Devices"

Applications

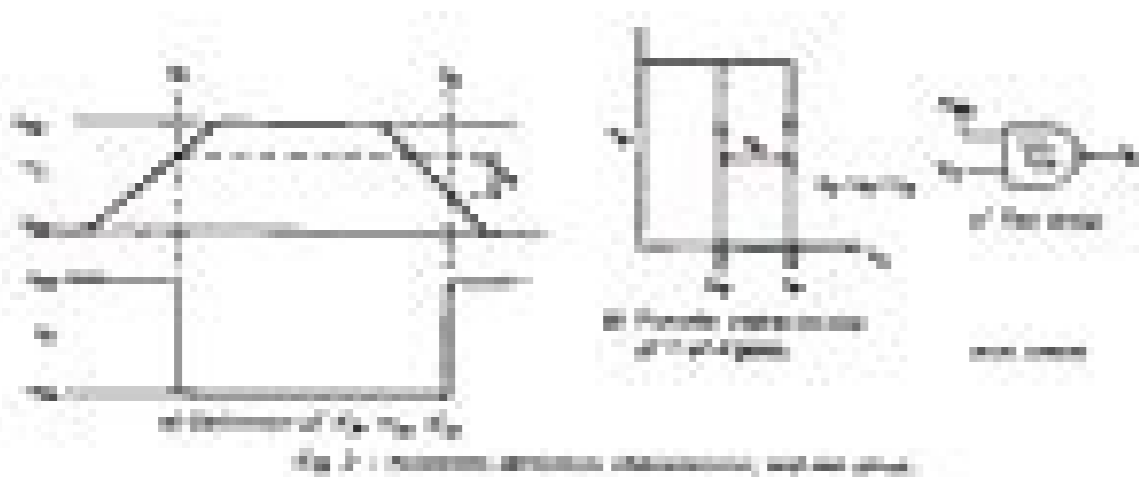
- Wave and pulse shaping
- High noise environment systems
- Monostable multivibrators
- Astable multivibrators
- Switch logic



Recommended Operating Conditions

The maximum absolute, limited operating conditions should be observed in the operation of devices within the following ranges:

PARAMETER	MIN.	MAX.	UNIT
Supply Voltage Range (VDD - Full Package Data Range)	5	15	V



CD4011, CD4012, CD4013 Types

PARAMETER	MIN.	MAX.	UNIT
Supply Voltage Range (VDD - Full Package Data Range)	5	15	V
Propagation Delay (tpd)	50	100	ns
Input Resistance (Rin)	> 100	> 100	MΩ
Output Resistance (Rout)	100	100	Ω
Output Current (IOL)	10	10	mA
Output Current (IOH)	10	10	mA
Power Dissipation (PD)	400	400	mW
Operating and Storage Junction Temperature Range (Tj, Tstg)	-55	+125	°C

CDIL
 Continental Device India Limited
 An ISO 9001, ISO 9002 and ISO 14001 Certified Company

NPN SILICON PLANAR TRANSISTOR

CD9011 TO-92 CBE

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector-Emitter Voltage	VCE0	30	V
Collector-Base Voltage	VCB0	30	V
Emitter-Base Voltage	VEB0	5.0	V
Collector Current	IC	100	mA
Power Dissipation	PD	400	mW
Operating and Storage Junction Temperature Range	Tj, Tstg	-55 to +125	°C

SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector-Emitter Voltage	IC=10mA, VBE=0	30	-	-	V
Collector-Base Voltage	IC=10mA, VBE=0	30	-	-	V
Emitter-Base Voltage	IC=10mA, VBE=0	5.0	-	-	V
Collector Cut-off Current	ICBO	10	50	100	nA
Emitter Cut-off Current	IEBO	10	50	100	nA
DC Current Gain	hFE	IC=1mA, VCE=10V	-	-	-
Collector-Emitter Saturation Voltage	VCE(sat)	IC=10mA, IB=1mA	-	-	-

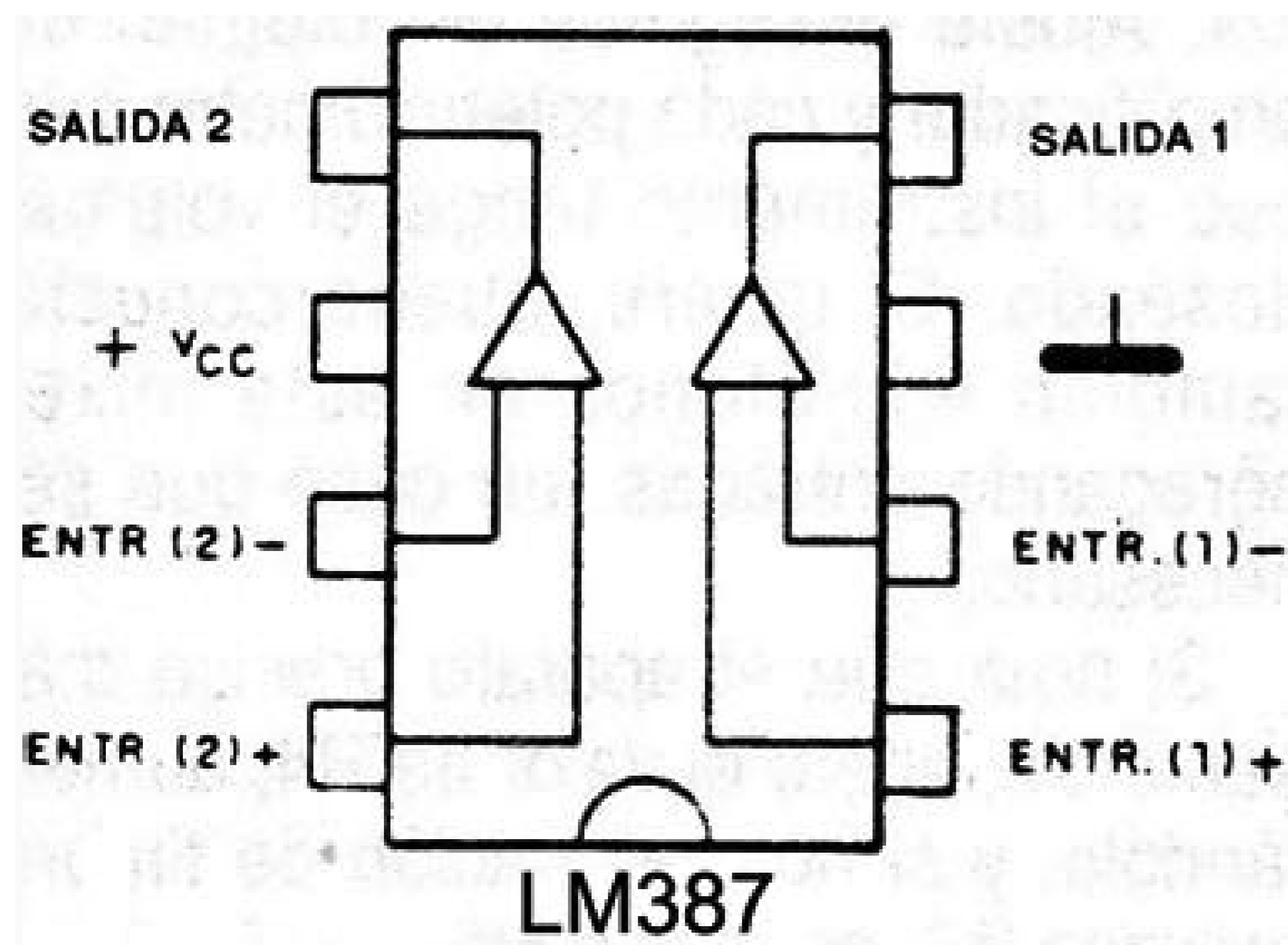
Dynamic Characteristics

SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Capacitance	Cob	VCE=10V, f=1MHz	-	-	pF
Transition Frequency	fT	VCE=10V, IC=10mA, f=100MHz	-	-	MHz

Noise Figure

SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
NF	VCE=10V, IC=10mA, f=1000	-	-	4.0	dB

MFR CLASSIFICATION: D 25-44, E 40-55, F 54-65, G 72-105, H 87-145, I 132-195, J 192-275



S: Type of actuator: Standard; Circuit: spst-no; CLASSIFICATION OF CONTACTS @ Voltage: 0.02 A to 15 V DC; Type of assembly: surface mount; Operational strength: 260GF; Orientation: vertical; Scheme: 6.50 mm x 6.40 mm; Switch function: OFF-MOM; Illumination: Without illumination; Lead-free state: The KM4211 uses only 208.µa of supply current per amplifier and is designed to operate from the $\pm 2.75V$ sources. CD4047 Datasheet pdf: Loan diagrams of 15 pages (continuation) * ASTABLE 5 * ASTABLE 4 * + TRIGGER 8 * -Trigger 6 VDD FF1 DQ CI R1 R2 CD4047BMS * RETRIGERGER 12 ** VDD 3 RC Common CTC 1 2 RTC OSC OUT 13 VDD 14 VSS 7 EXTERNAL * 9 RESET * Protected Network Protection Inputs S DQ FF2 VSS CL QR FF4 CL R R1 S2 DQ FF4 CL R R1 S2 DQ FF4 CL R Terminal 3 is more sensitive to downloads Electric static. S: Resistance (ohms): 240; Power (watts): 0.6 W; Tolerance: $\pm 1\%$; Packing: Ribbon and reel (TR); Composition: thin film; Temperature coefficient: $\pm 50ppm / \text{Å}^\circ C$; Lead without lead: unleaded; Status of RoHS: Compatible with RoHS. The KM4211 offers a high bipolar performance at a low CMOS price. Additional precautions of manipulation are recommended. MC4558V : 12065C151at2A: 12065C151Diode GAOB Beam Lead Schottky: 250pf 1206 (3216 Métrica) CaseMics Condenser; CAP CER 150PF 50V X7R 1206. 71V3559: ZBT (Zero Bus Turnaround) SRAM. 1 A)DIFFUSED JUNCTION! Baja escape! Bajo cost! Outpat the overvoltage capacity! Typical I go less than 5.0 µA! Case: Copper Box and Certhemically Sealed Terminal Components: Isolated Windshield Wire Cable Isolated Cable: Box (Inverse Units are available under request and are appointed by a suffix "R", it is Say, or PFW5004R) Polarity: The red color is the same. FV2636: Inspection scope. The km4211. The input voltage range extends 300mV by negative rail and 1.2V below positive rail. 3.3 VOLT TIME SLOT INTERCHANGE DIGITAL SWITCH x 2,048 x 2,048 channel * switch at 8.192 Mb/s Per variable channel or Flow delay Automatic identification of ST-BusA@ / GCI interfaces Accepts 2,048 MB / S flows, 4,096 MB / its 8,192 MB / S automatic measurement of frame scroll delay Programming by frame delay channel by flow. 1.3V Maximum desertion at full load Rapid response Transitory Limiting current Built-in closure Built-in rejection of good noise Package of 3 terminals Adjustable or fixed / 5.0V: The free finishing finish SOT89-3L / ROHS compliance for Lead-free products (Note 1) is a fixed-mode regulator or adjustable positive low desertion with 0.6A output current capacity. Figure 2. S: Capacitance: 150PF; TENSION - NOMINAL: 50 V; Tolerance: $\pm 5\%$; Package / Case: 1206 (3216 Métrica); Temperature coefficient: x7r; Packing: Ribbon and reel (TR); : -; Cable spacing: -; Operating temperature: $-55^\circ C \sim 125^\circ C$; Type of assembly: surface mount, MLCC; Lead Free. EVQ-PhK03T: SPST-NO OFF-MOM 0.02A @ 15VDC tactile switch; 6mm switch 260GF H = 3.1mm SMD. KM4211: 35MHz dual rro amplifier. 5-1879222-0: 61.9 OHM 0.063W, 1 / 16W chip resistance - surface mount; Res 61.9 Ohm 1/16 W 0.1% 0603. MBB02070C2400FRP00: 240 Ohm 0.6W through holes resistors; RES 240 OHM 0.6W 1% 2007. S: Detection distance: 0.125 "(3.18 mm); method of detection: transmissive; output configuration: extraction resistance, búfer; type of assembly: through Of the drill; Current - Supply: 7.5 MA; Tension - Supply: 4.5 V ~ 16 V; Pack / Case: PCB assembly. He is an ultra-low, low-cost, feeding dual feed amplifier Voltage 170038-1 - Free pendant terminal (online) - Rapid connections, quick disconnect connectors, free pendant interconnection (online); Conn Unins Recept 16-20AWG 0.187. The MC4558V combines all the protruding of the MC1458 and, in addition, three times the drive gain bandwidth of the industry's core. OPB480L11 - Photoswitches - Slot type - OUTPUT SENSOR SWITCH LA GICA A PTICA SLOTTED -; PTIC SENSOR SWITCH SLOTTED. PFW5005: 50A 1/2 Washer LEAD Press-fit Press-fit .O / 1 .O / her v3.3 / w Mes HGOURHT-Wolf SIHTHS DNAF: Eton Tetcudan lacinhet Pid Seconds 0004 A à à [ic ZIMTH DNA) a (3FF DNA 1F SPAF-Pilf Rof Rof Craft Larted V3, HCDIMS Latighid Egnahcher (020.0: SSENKCHT BAT; KLUB; Elamef; Rednev; - Arose - s. linyMOC SHOR: SETATS SHORT: ERFE DELL: NOERPTEMMOB:: EUCHOOPTMOB:: EUCHOOPTMOB:: EUCHOOPTEMB; ecnarelot; W61 / 1, W360.0;) sttaw (rewop ; 9.16.) smha (ethatsiser: s .sdol rotted_Gnixim: 7302e4am lc Lc lc np lc q np lc lc lc lc lc lc lc qd. 1r 2r 2r 1R rewop gnitar eropt's noitpunal gntoma snAifilpm HCAMPMIN DNATHEPTHAB DNAIX DNATHEPT TIMHABT_HY

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